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Teri McDonald 9/1/05

DOCUMENT(S) FAXED:

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Re Applic of	Hiroyuki Akatsu, et al.
Docket No.	FIS920030411US1
Serial No.	10/708,340
Filing Date	2/25/04
Attorney	H. Daniel Schnurmann

Attached: Response to Restriction Requirement

PLEASE DELIVER TO: Dao H. Nguyen
EXAMINER: ART UNIT: 2818
CONFIRMATION NO.: 2339
PHONE NO:
FAX NO: 571-273-8300

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PATENTS AND TRADEMARKS, Alexandria VA. 22313, on September 1, 2005 by: Teri McDonald


Signature & Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: Horoyuki Akatsu, et al.	Date: September 1, 2005
Serial Number: 10/708,340	Examiner: Dao H. Nguyen
Filed: 02/25/2004	Group Art Unit: 2818
Title: Structure and Method of Self-Aligned Bipolar Transistor Having Tapered Collector	IBM Corporation D/18G, B/300, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner of Patents and Trademarks
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action dated August 24, 2005.

The Examiner in the aforementioned Office Action has required restriction under
35 U.S.C. § 121, stating that the claims belong to:


GROUP I, Claims 1-10, drawn to semiconductor devices, and

GROUP II, Claims 11-20, drawn to a method of manufacturing semiconductor
devices.

Applicants elect to prosecute the invention of GROUP I, consisting of Claims 1-10 drawn to semiconductor devices, and withdraw from consideration the claims forming GROUP II, as being drawn to non-elected invention, without prejudice to the Applicants' right to file a Divisional or Continuation or Continuation-in-Part patent application for the withdrawn claims.

The election is made without traverse.

Respectfully submitted,
HOROYUKI AKATSU, ET AL.

By: 
H. Daniel Schnurmann, Agent
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